



PATENT
Att'y Dkt.: 2207/10607

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

SOLOMON et al

Serial No.: 09/892,566

Filed: June 28, 2001

For: POWER REDUCTION FOR PROCESS
FRONT-END BY CACHING DECODED
INSTRUCTIONS

Assignee: Intel Corporation

Examiner: G.J. Portka

Art Unit: 2188

9/13
Edna
5-20-04

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MAY 12 2004

Technology Center 2100

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action, dated February 13, 2004, kindly amend the application as follows: